THE UNITED STATES PATENT AND TRADEMARK OFFICE Seong Min Seo, Young Suk Chung, Jon Sik Paek, Jae Hun Ku, Jae Hak Amkor Technology, Inc. Assignee: Semiconductor Package Including Stacked Chips Title: March 23, 2001 RECEIVED Filing Date: 09/816,599 Serial No .: 2811 AUG 2 0 2002 Group Art Unit: Not Yet Assigned Examiner: Technology Center 2600 AB-1111 US Docket No.: San Jose, California September 24, 2001 COMMISSIONER FOR PATENTS RECEIVED Washington, D.C. 20231 FEB 0 5 2002 INFORMATION DISCLOSURE STATEMENT UNDER 37 CFR § 1.97(b) OFFICE OF PETITIONS Dear Sir: Pursuant to 37 C.F.R. § 1.56, § 1.97 and § 1.98, Applicants wish to call the documents listed on the accompanying PTO Form-1449 to the Examiner's attention. Copies of these documents are enclosed. Citation of these documents shall not be construed as: an admission that the documents are necessarily prior art with respect to the 1. instant invention; a representation that a search has been made, or 2. an admission that the information cited herein is, or is considered to be, 3.

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material to patentability as defined in § 1.56(b).

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Serial No. 09/816,599

Applicants submit that no fee is required. If, however, a fee is necessary under 37 C.F.R. 1.97(c), then such fee and any other appropriate fee may be charged to Deposit Account No. 19-2386.

I hereby certify that this correspondence is being deposited with the United States Postal Service as First Class Mail in an envelope addressed to: Commissioner for Patents, Washington, D.C. 20231, on September 72001.

Attorney for Applicants

Respectfully submitted,

Attorney for Applicants

Reg. No. 34,691

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*EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include copy of this form with your communication to applicant. RECEIVED

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